

## AMENDMENTS

Please amend the application as indicated hereafter.

### In the Claims

Please substitute the following clean copy text for the pending claims of the same number.

1. (Twice Amended) A wireless communication device, comprising:
  - a serial interface configured to accept input data at a first voltage, the input data including a control signal for an integrated circuit component;
  - a local level shifter configured to accept a portion of the control signal at the first voltage, the local level shifter configured to maintain a shifted control signal, where the shifted control signal is at the operating voltage of the integrated circuit component, and where the shifted control signal controls the operation of the integrated circuit component; and
  - a data latch configured to accept the portion of the control signal at the first voltage level from the serial interface, the data latch configured to output the portion of the control signal at the first voltage to at least the local level shifter, where the local level shifter is configured to maintain the shifted control signal in an integrated circuit while the integrated circuit is operating in a shutdown mode.
2. (Twice Amended) The wireless communication device of claim 1, further comprising:
  - a second local level shifter, the second local level shifter configured to accept a second portion of the control signal at the first voltage, the second local level shifter being configured to maintain a second shifted control signal, where the second shifted control signal is at the operating voltage of a second integrated circuit component, where the second shifted control signal controls the operation of the second integrated circuit component;
  - a second data latch, the second data latch configured to accept the second portion of the control signal at the first voltage level from the serial interface, the second data

latch configured to output the second portion of the control signal at the first voltage level to the second local level shifter, where the second local level shifter is configured to maintain the second shifted control signal in the integrated circuit while the integrated circuit is operating in the shutdown mode.

7. (Twice Amended) The wireless communication device of claim 1, where the integrated circuit is a radio frequency integrated circuit.

8. (Once Amended) The wireless communication device of claim 1, where the integrated circuit component is one of a synthesizer, demodulator, downconverter and modulator/upconverter.

10. (Twice Amended) A system for maintaining programming information in an integrated circuit during a shutdown mode, comprising:

means for accepting input data at a first voltage, the input data including control signals for a plurality of integrated circuit components;

means for distributing the control signals to the plurality of integrated circuit components;

means for converting the control signals at the first voltage to shifted control signals at the operating voltage of the integrated circuit components; and

means for maintaining the shifted control signals in the integrated circuit while the integrated circuit is operating in the shutdown mode.

17. (Once Amended) The system of claim 10, where one of the integrated circuit components is one of a synthesizer, demodulator, downconverter and modulator/upconverter.

18. (Twice Amended) A method for maintaining programming information in an integrated circuit during a shutdown mode, comprising the steps of:

accepting integrated circuit input data at a first voltage, the input data including control signals for a plurality of integrated circuit components;  
distributing the control signals to the plurality of integrated circuit components;  
converting the control signals at the first voltage to shifted control signals at the operating voltage of the integrated circuit components; and  
maintaining the shifted control signals in the integrated circuit while the integrated circuit is operating in the shutdown mode.

25. (Once Amended) The method of claim 18, where one of the integrated circuit components is one of a synthesizer, demodulator, downconverter and modulator/upconverter.

26. (Twice Amended) A computer readable medium having a program for maintaining programming information in an integrated circuit during a shutdown mode, comprising:

logic for accepting input data at a first voltage, the input data including control signals for a plurality of integrated circuit components;

logic for distributing the control signals to the plurality of integrated circuit components;

logic for converting the control signals at the first voltage to shifted control signals at the operating voltage of the integrated circuit components; and

logic for maintaining the shifted control signals in the integrated circuit while the integrated circuit is operating in the shutdown mode.

33. (Once Amended) The program of claim 26, where one of the integrated circuit components is one of a synthesizer, demodulator, downconverter and modulator/upconverter.